UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 7,132,299 B2

Page 1 of 1

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INVENTOR(S)

: Yates et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 3, line 19, delete "an memory cell" and insert -- a memory cell --, therefor.

In column 3, line 21, delete "an memory cell" and insert -- a memory cell --, therefor.

In column 14, line 6, in Claim 52, delete "includes" and insert -- with an --, therefor.

Signed and Sealed this

Sixth Day of February, 2007

JON W. DUDAS Director of the United States Patent and Trademark Office